Course Number and Name

BEC001 - ADVANCED COMPUTER ARCHITECTURE

Course Objectives

- To make students know about the Parallelism concepts in Programming
- To give the students an elaborate idea about the different memory systems and buses.
- To introduce the advanced processor architectures to the students.
- To make the students know about the importance of multiprocessor and multi-computers.
- To study about data flow computer architectures

Prerequisites	Co-requisites
BEC302-Principles of digital electronics	BEC502-Microprocessor & Microcontroller

Course Outcomes (COs)

CO1: Demonstrate concepts of parallelism in hardware/software.

CO2 : Discuss memory organization and mapping techniques.

CO3 : Describe architectural features of advanced processors.

CO4 : Interpret performance of different pipelined processors.

CO5: Explain data flow in arithmetic algorithms

CO6 : Development of software to solve computationally intensive problems.

Student Outcomes (SOs) from Criterion 3 covered by this Course

COs/SOs	a	b	c	d	e	f	g	h	i	j	k
CO1	H					M				M	
CO2	M	M	H					L			
CO3	M		H	H					H		
CO4	M				H		M				M
CO5		M			M				M		
CO6						H					